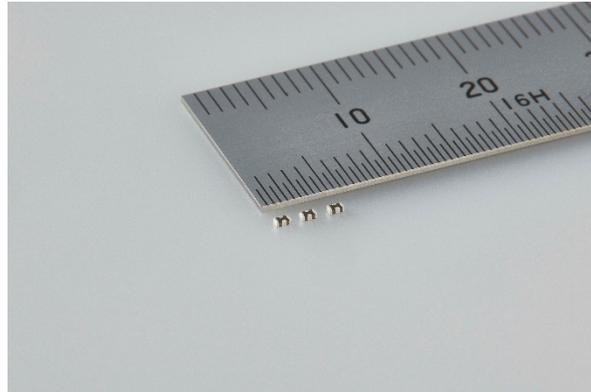


For immediate release

TAIYO YUDEN Reduces the Thickness of 1005-Size Three-Terminal Multilayer Ceramic Capacitors by 23%

Reduced thickness of 0.5 mm with a capacitance of 10 μ F, contributing to the realization of thinner devices



TOKYO, March 13, 2019—TAIYO YUDEN CO., LTD. has announced the launch of its 1005-size three-terminal multilayer ceramic capacitor A3K105BBJ106MR (1.0×0.5×0.5 mm, with height as the maximum value, capacitance of 10 μ F, and rated voltage of 4.0 V).

Three-terminal multilayer ceramic capacitors, which have a lower ESL than the popular two-terminal multilayer ceramic capacitors, can reduce impedance in the high-frequency range and contribute to the stable operation of ICs that are driven at high speed. The thickness of the new ceramic capacitor has been reduced by approximately 23% compared to our conventional product, A3K105KBJ106MV (1.0×0.5×0.65 mm, with height as the maximum value), contributing to the further reduction in thickness of small, thin digital devices.

This product is to be used as a decoupling capacitor for power supply lines for ICs in response to demand for smaller and thinner devices, such as IoT-related devices, typified by smartphones and wearable devices.

Production of this multilayer ceramic capacitor will commence at the company's Tamamura Plant (Tamamura-machi, Sawa-gun, Gunma Prefecture, Japan) starting from March 2019 at a production rate of 5 million units per month, with a sample price of 10 yen per unit.

Technology Background

Multilayer ceramic capacitors are placed near ICs mounted in smartphones or wearable devices for the purpose of decoupling. As devices increase in functionality, the speed of the ICs incorporated in them has also increased, requiring decoupling capacitors with a lower ESL placed around such ICs to ensure their stable operation. Furthermore, to realize thin devices despite increases in the number of features and the use of larger batteries, there is growing demand for thinner electronic components to be incorporated into them.

Multilayer ceramic capacitors tend to have a higher impedance and deteriorate in performance in the high-frequency range, owing to ESL influence. Three-terminal multilayer ceramic capacitors, which have a lower ESL than the popular two-terminal multilayer ceramic capacitors, have the advantage of reduced impedance in the high-frequency range. On this occasion, we have achieved an approximately 23% reduction in thickness compared to our conventional product by sophisticating materials technology and sheet lamination technique, thus realizing a large capacity of 10 μ F and a thickness of 0.5 mm at the same time with 1005-size three-terminal multilayer ceramic capacitors.

Moving forward, we will continue to make our multilayer ceramic capacitors even smaller and thinner, as well as enhance their capacitance in response to market demand.

■Application

Decoupling application for power supply lines for ICs in response to demand for smaller and thinner devices such as IoT-related devices, typified by smartphones and wearable devices.

■Characteristics

Part number	Rated voltage	Temperature characteristics	Capacitance	Capacitance tolerance	Length (L) [mm]	Width (W) [mm]	Thickness (T) [mm]
A3K105BBJ106MR	4.0V	X5R	10 μ F	\pm 20%	1.0 +0.15/ -0.05	0.5 +0.15/ -0.05	0.5 (max.)